Code No: R203204C (R20) (SET -1

## III B. Tech II Semester Regular Examinations, July -2023 EMBEDDED SYSTEMS

(Electronics and Communication Engineering)

Max. Marks: 70

[7M]

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		Answer any FIVE Questions ONE Question from Each unit	
		All Questions Carry Equal Marks	
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1	۵)	<u>UNIT-I</u> Explain the election of embedded express with examples	[7] (1)
1.	a)	Explain the classification of embedded systems with examples.	[7M]
	b)	List out major application areas of embedded systems with examples.  (OR)	[7M]
2.	a)	What is the role of quality attribute in the embedded system development context? Explain the different Quality attributes to be considered in an embedded system design.	[7M]
	b)	Explain Time-to-market? What is its significance in product development?	[7M]
		<u>UNIT-II</u>	
3.	a)	Explain different ways of communication between the serial communication ports.	[7M]
	b)	Illustrate the watchdog timer for firmware execution supervision.	[7M]
1	۵)	(OR) Describe the sequence of operation for communicating with an I2C slave device.	[7] (1)
4.	a)		[7M]
	b)	Explain the merits and limitations of Parallel port over Serial RS-232 interface.	[7M]
5.	a)	<u>UNIT-III</u> Discuss the different 'embedded firmware design' approaches in detail.	[7M]
٥.			
	b)	Explain the advantages of 'High Level language' based 'Embedded firmware' development.  (OR)	[7M]
6.	a)	What is Interrupt? Explain its properties and What is its role in embedded	[7M]
0.	a)	application development?	[/1 <b>V1</b> ]
	b)	What are the different types of pre-processor directives available in 'Embedded C'? Explain them in detail.	[7M]
		<u>UNIT-IV</u>	
7.	a)	What is kernel? What are the different functions handled by a general purpose kernel. Explain in detail.	[7M]
	b)	What is the difference between 'Hard' and 'Soft' real-time systems? Give an	[7M]
		example for 'Hard' and 'Soft' Real-Time kernels.	
8.	۵)	(OR) Discuss how Threads and Processes are related. What parameters are common to	[7](1)
0.	a)	Process and Threads? Explain in detail.	[7M]
	b)	Explain the various factors to be considered for the selection of scheduling criteria.  UNIT-V	[7M]
9.	a)	Explain the various elements of an embedded system development environment.	[7M]
	b)	Explain the various details held by a Map file generated during the process of cross-	[7M]
		compiling an Embedded C project.	
10		(OR)	[ <b>773 5</b> 7
10.	a)	What is ROM emulation? Explain In Circuit Emulator (ICE) based debugging in detail.	[7M]

b) Explain how a digital camera implemented using an embedded operating system.

1 of 1

Time: 3 hours

Code No: R203204C (R20) (SET -2)

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Max. Marks: 70

[7M]

Answer any **FIVE** Questions **ONE** Question from **Each unit** All Questions Carry Equal Marks \*\*\*\* **UNIT-I** Explain the different characteristics of embedded systems in detail. 1. [7M] a) Discuss the purpose of embedded systems and applications.. b) [7M] What is the difference between General Purpose Processor (GPP) and 2. [7M] a) Application Specific Instruction Set Processor (ASIP)? Give an example for each. b) Describe the quality attributes of embedded systems. [7M] 3. a) What are the different modes of operations in Serial Interface port? Explain in [7M] b) Explain the role of Reset circuit in Embedded System. [7M] 4. Explain the role of Real Time Clock (RTC) in Embedded System. [7M] Write in detail about the merits and limitations of Parallel port over Serial RSb) [7M] 232 interface. **UNIT-III** What is the difference between 'Super loop' based and 'OS' based embedded 5. [7M] firmware design? Which one is the better approach? Explain it. Explain the 'High Level language' based 'Embedded firmware' development [7M] b) technique. (OR) 6. What are Interrupt Vector Address and Interrupt Service Routine (ISR)? How [7M] are they related? Explain it. Explain the different bit manipulation operations supported by 'Embedded C'. b) [7M] **UNIT-IV** 7. a) What is kernel space and user space? How is kernel space and user space [7M] interfaced? Explain it. Explain the different computational models in embedded system design. b) [7M] (OR) 8. Explain the concept of 'multithreading'. What are the advantages of [7M] a) multithreading. What all activities are involved in context switching? Explain in detail. [7M] b) **UNIT-V** 9. Discuss the role of Integrated Development Environment (IDE) for Embedded a) [7M]

(OR)

10. a) What is the difference between a simulator and an emulator? Explain in detail. [7M]

Differentiate between Intel Hex and Motorola Hex file format.

b) Discuss the case study of embedded system for a smart card. [7M]

1 of 1

b)

Software Development.

Time: 3 hours

Code No: R203204C (R20) (SET -3

### III B. Tech II Semester Regular Examinations, July -2023 EMBEDDED SYSTEMS

(Electronics and Communication Engineering)

Time: 3 hours Max. Marks: 70 Answer any **FIVE** Questions **ONE** Question from **Each unit** All Questions Carry Equal Marks \*\*\*\* **UNIT-I** Illustrate an application-specific Embedded system with suitable example. 1. [7M] a) Explain in detail about the typical embedded system-core of the embedded b) [7M] system. (OR) 2. What is Sensor? Explain its role in Embedded System Design? Illustrate with [7M] an example. Describe what embedded firmware is and its role in embedded systems. b) [7M] **UNIT-II** 3. Explain the role of the analog electronic components resistor, transistor, [7M] capacitor and diode in embedded hardware design. Draw a circuit used in embedded application using these components. What is open collector? State its significance in embedded hardware [7M] development. (OR) 4. Explain serial communication using I2C, CAN and USB bus in detail. [7M] Discuss the role of Watchdog Timer in Embedded System. b) [7M] **UNIT-III** 5. Explain the limitations/drawbacks of 'Assembly language' based Embedded a) [7M] firmware development. Describe the various steps involved in the assembling of an assembly language [7M] b) program. (OR) 6. What is the difference between compiler and cross-compiler? Explain in detail. [7M] Define task control block (TCB)? Explain the structure of TCB. [7M] b) **UNIT-IV** 7. a) What is the difference between a General Purpose kernel and a Real-Time [7M] kernel? Give an example for both. b) What is the difference between Interrupt Service Routine and Normal Service [7M] Routine? Explain in detail. (OR) 8. Explain how multithreading can improve the performance of an application [7M] with an illustrative example. What is Computational model? Explain its role in hardware software cob) [7M] design. **UNIT-V** 9. What are the different files generated during the cross-compilation of an [7M] Embedded C file? Explain them in detail. Explain the advantages and limitations of simulator based debugging. b) [7M] (OR) Explain in detail about the On Chip Debugging (OCD). 10. a) [7M] Sketch the block diagram of digital camera and explain digital camera b) [7M]

implementation in an embedded operating system.

SET-4 **R20** Code No: R203204C

# III B. Tech II Semester Regular Examinations, July -2023 EMBEDDED SYSTEMS

(Electronics and Communication Engineering)

Max. Marks: 70 Time: 3 hours

#### Answer any FIVE Questions ONE Question from Each unit All Questions Carry Equal Marks **UNIT-I** Draw and explain the typical Embedded system architecture. 1. [7M] a) What is Actuator? Explain its role in Embedded System Design? Illustrate with [7M] b) an example. (OR) 2. What is Embedded Firmware? What are the different approaches available for [7M] Embedded Firmware development? Explain the quality attribute Throughput in the embedded system design [7M] b) context. **UNIT-II** Explain the different on-board communication interfaces in brief. 3. [7M] Explain the difference between I2C and SPI communication interface. b) [7M] (OR) 4. Explain the different external communication interfaces in brief. [7M] a) What is Electronic Design Automation (EDA) tool? Explain the role of EDA [7M] b) tools in embedded system design. UNIT-III 5. Explain the advantages of 'Assembly language' based Embedded firmware [7M] development. Explain 'library file' in assembly language context. What is the benefit of [7M] b) 'library file'? Explain it. (OR) What all precautionary measures need to be implemented in an Interrupt [7M] 6. a) Service Routine (ISR)? Explain in detail. Explain in detail about the device driver programming. b) [7M] **UNIT-IV** 7. Explain the commonly used thread standards for thread creation and [7M] management by different operating systems. Explain multiprocessing, multitasking and multiprogramming. b) [7M] (OR) 8. Explain the different types of non-preemptive scheduling algorithms. State the [7M] merits and de-merits of each. What is hardware software co-design? Explain the fundamental issues in [7M] b) hardware software co-design. **UNIT-V** 9. Explain the various details held by a List file generated during the process of [7M] cross-compiling an Embedded C project. Explain the role of Integrated Development Environment (IDE) for Embedded [7M] Software Development. (OR) What are the different techniques available for embedded firmware debugging? [7M] 10. a) Explain them in detail.

1 of 1

[7M]

Discuss mobile phone software for key inputs.

b)